



05/02/2006

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NO. 167 D002

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Manu Kashyap	(Depositor's name)
<i>Manu Kashyap</i>	(Signature)
May 2, 2006	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/616,623	07/10/2003	Aiguo Lu	03-0932/L13.12-0239	3364

TITLE OF INVENTION: OPTIMIZING IC CLOCK STRUCTURES BY MINIMIZING CLOCK UNCERTAINTY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/02/2006
EXAMINER	ART UNIT		CLASS-SUBCLASS		
KIK, PHALLAKA	2825		716-006000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Westman, Champlin & Kelly
2 _____
3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

LSI Logic Corporation

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Milpitas, California USA

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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Authorized Signature Henry GrothDate May 2, 2006Typed or printed name Henry GrothRegistration No. 39,696

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